FIG. 1

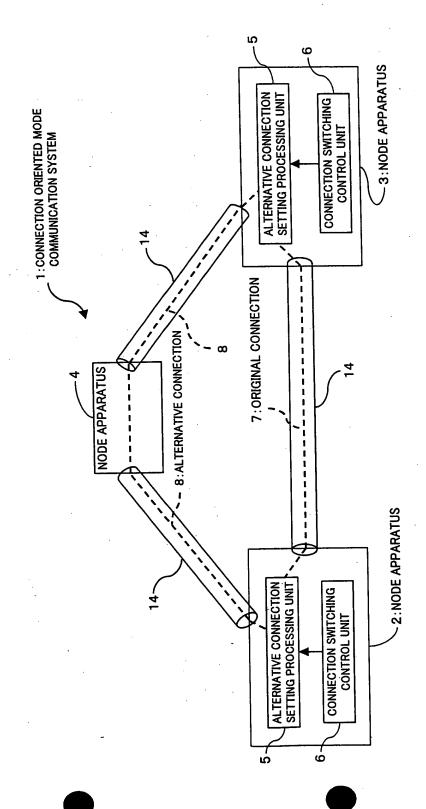
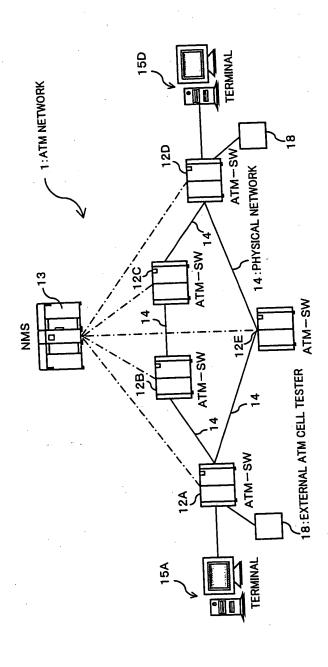
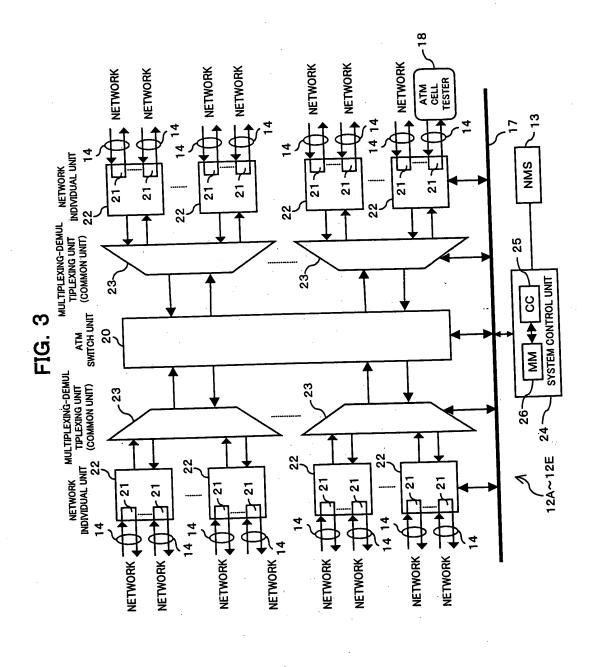


FIG. 2





23:COMMON UNIT OAM PROCESSING UNIT CHARGING/NDC PROCESSING UNIT 22(22A) PROCESSOR UPC/NPC PROCESSING UNIT HEADER INVERSE CONVERSION UNIT (VPI/VCI — I-ICID) HEADER CONVERTING UNIT (VPI/VCI → I-ICID) MÉMORY CELL SELECTING UNIT CELL COPY UNIT PHYSICAL TERMINATING UNIT 21 OUTPUT NETWORK IF UNIT INPUT NETWORK IF UNIT PORT n▲ PORT 0 PORT n . PORT 0-14:PHYSICAL NETWORK 14:PHYSICAL NETWORK

FIG. 5

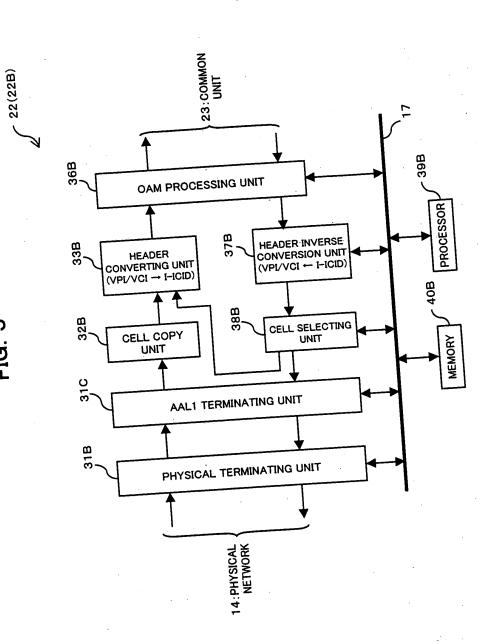


FIG. 6

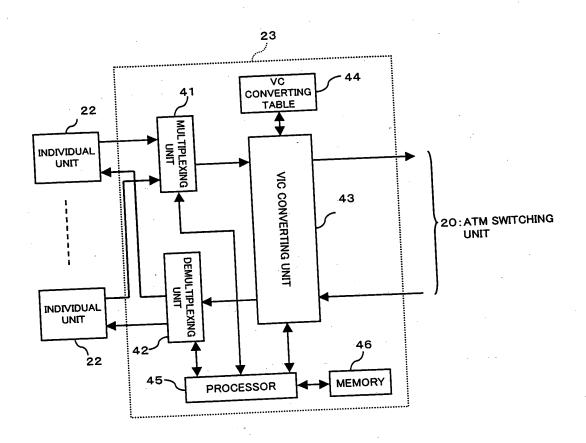


FIG. 7(a)



			0.000	
Γ	TARGET	COPY SOURCE	COPY DESTINATION	Alternative
١	VPI/VCI	VPI/VCI	VPI/VCI	Route
L		77. (1.00	51/100	0
١	0/32	50/100	317 100	
ł			:	
L		<u> </u>	<b>A</b>	<b>∧</b>
	∕∕\	<b>/</b>	<i>?</i> `	ſ
	_(	/ 51b	5 <sup>1</sup> 1c	51d
	`51a	315	- / -	

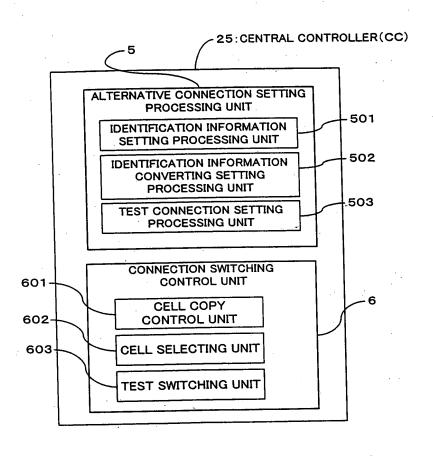
FIG. 7(b)

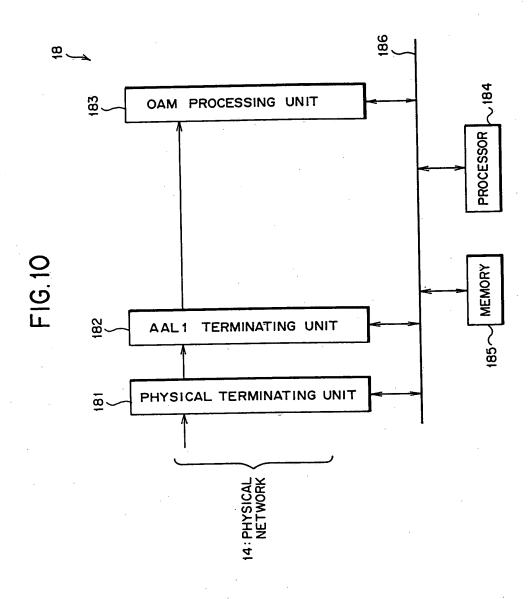
	51d
0	Alternative Route NOT UTILIZED
1	Alternative Route UTILIZED

RECEIVING SIDE HEADER INVERSE CONVERSION TABLE

I-ICID-A"	VPI/VCI	Enable
100	0/32	1
101	0/32	0

FIG. 9





## FIG. 1

PARAMETER	CONTENTS	NOTE
CV.	CODING VIOLATION	ABOUT BIT ERROR, PRESENT OR NOT
ES	ERRORED SECOND	NUMBER OF DETECTED EVENTS OF CV, AIS AND SO ON
SES	SEVCRELY ERRORED SECOND	ERROR SUCH AS CV, AIS AND SO ON LARGER THAN ES
SAS	SEF/Ais SECOND	
Aiss	Ais SECOND	
UASP	UNAVAILABLE SECOND	

FIG. 12

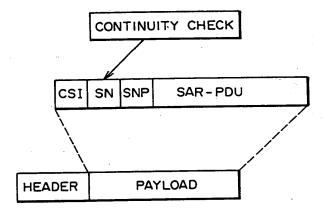


FIG.13

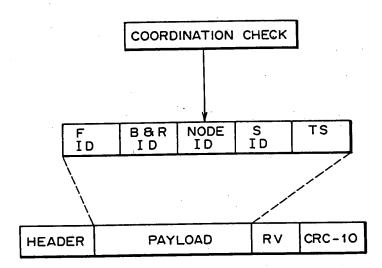


FIG.14

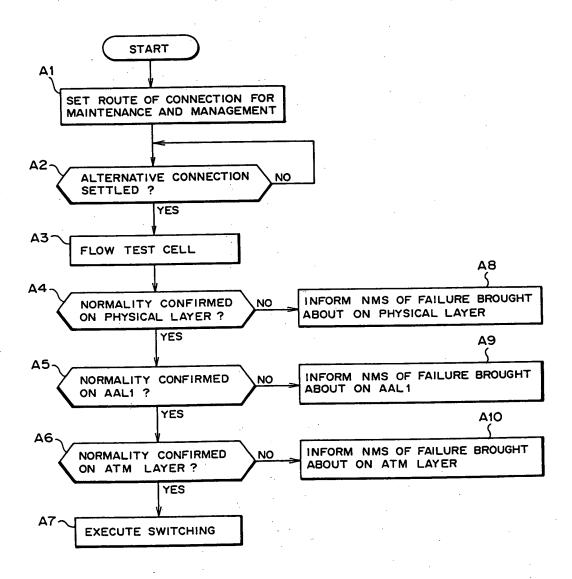
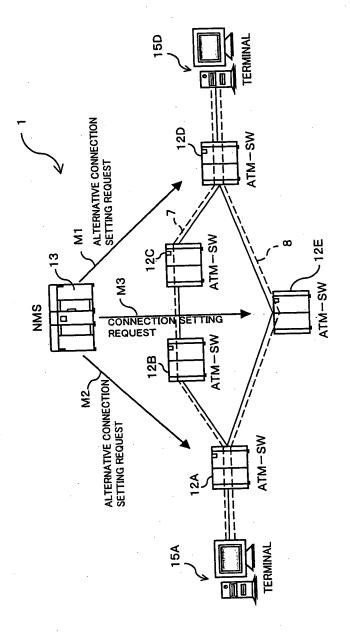
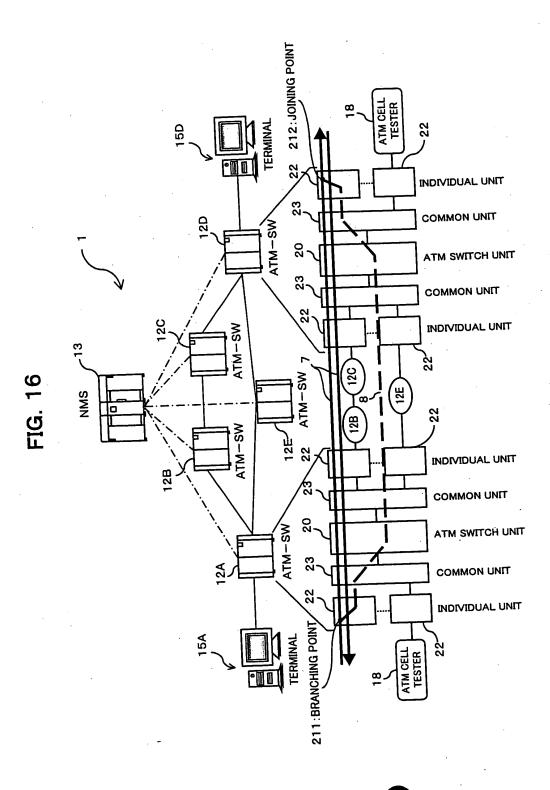
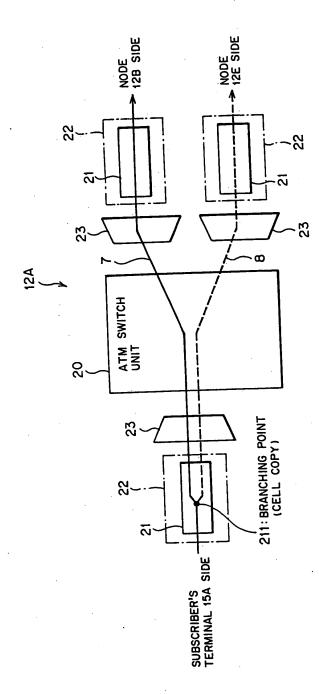


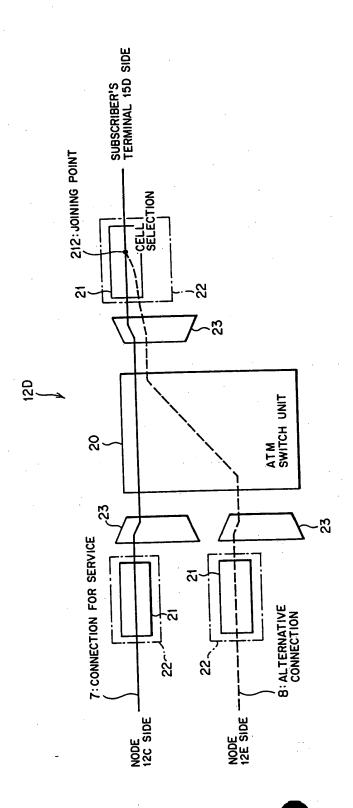
FIG. 15

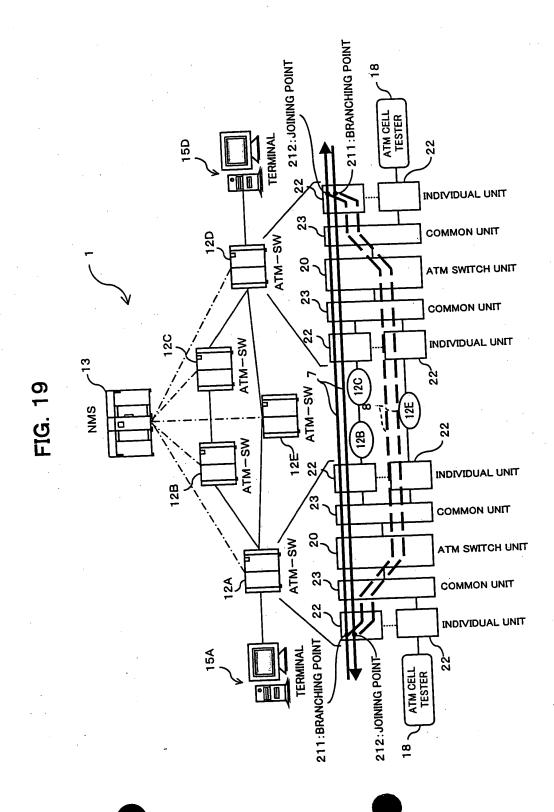


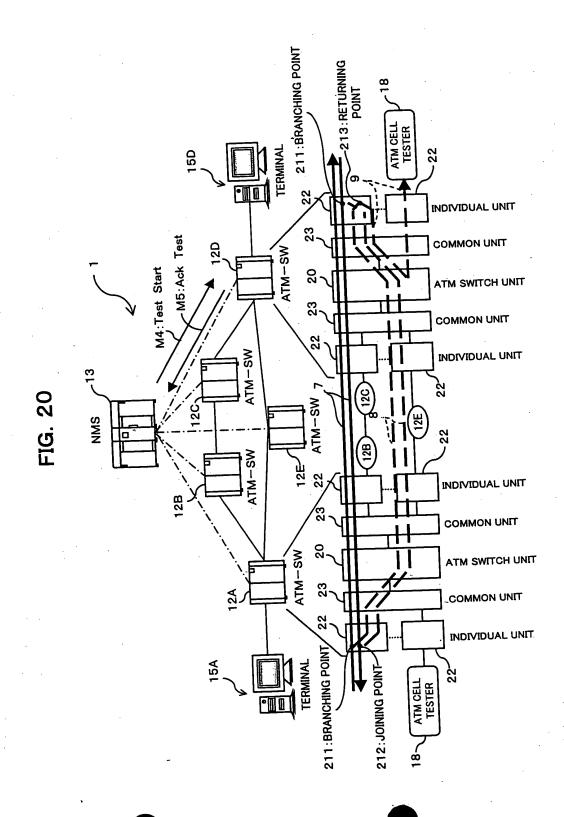


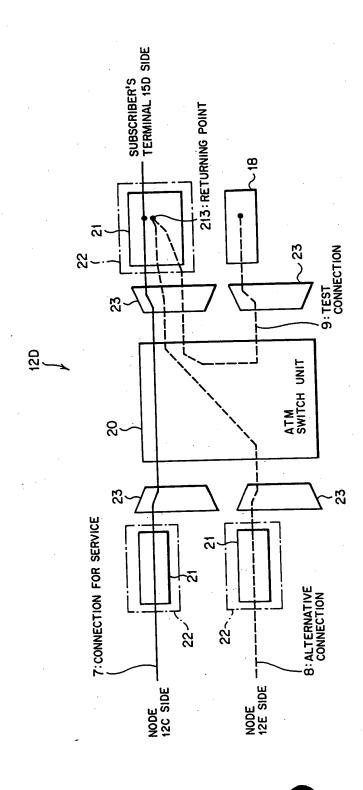


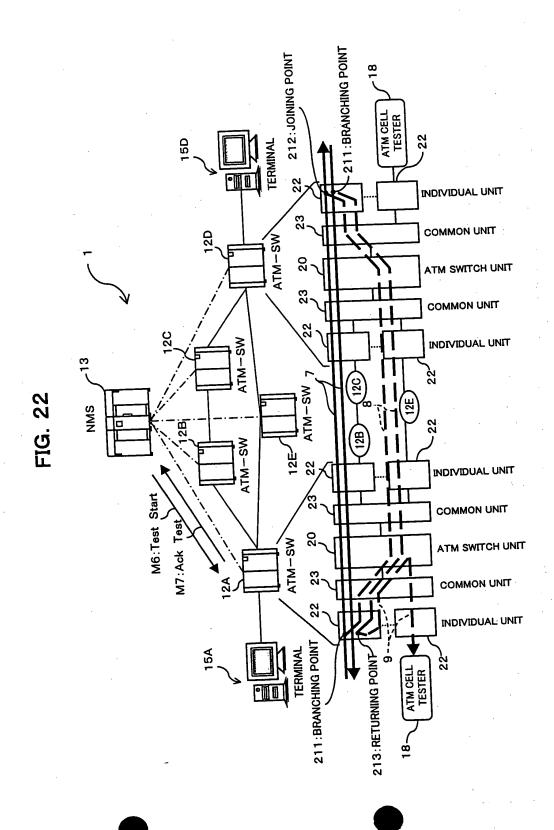
F1G.18







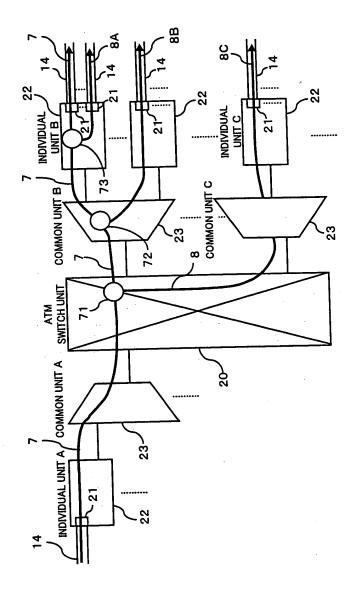




~211:BRANCHING POINT 212: JOINING POINT ATM CELL TESTER TERMINAL INDIVIDUAL UNIT Mg:Bridge and Roll COMMON UNIT ATM-SW 20 ATM SWITCH UNIT COMMON UNIT ATM-SW INDIVIDUAL UNIT ATM-SW NMS -25 ATM-SW 12元 22 M8:Bridge and Roll INDIVIDUAL UNIT COMMON UNIT 20 ATM-SW ATM SWITCH UNIT 6 **X** COMMON UNIT INDIVIDUAL UNIT TERMINAL 211: BRANCHING POINT 212: JOINING POINT-22 ATM CELL TESTER 18

FIG. 23

FIG. 24



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FIG. 25

